

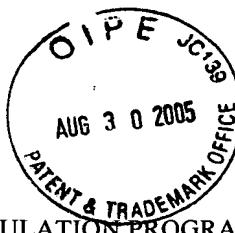
Docket No. 217804US2

IN RE APPLICATION OF: Sanae ITO, et al.

SERIAL NO: 10/028,319

FILED: December 28, 2001

FOR: SIMULATION METHOD, SIMULATION PROGRAM, AND SEMICONDUCTOR DEVICE
MANUFACTURING METHOD EACH EMPLOYING BOUNDARY CONDITIONS



COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment in the above-identified application.

No additional fee is required
 Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
 Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	15	MINUS	20	0	x \$50 =	\$0.00
INDEPENDENT	3	MINUS	3	0	x \$200 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS					+ \$360 =	\$0.00
TOTAL OF ABOVE CALCULATIONS						\$0.00
<input type="checkbox"/> Reduction by 50% for filing by Small Entity						\$0.00
<input type="checkbox"/> Recordation of Assignment					+ \$40 =	\$0.00
					TOTAL	\$0.00

A check in the amount of \$0.00 is attached.
 Credit card payment form is attached to cover the fees in the amount of \$0.00
 Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
 If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

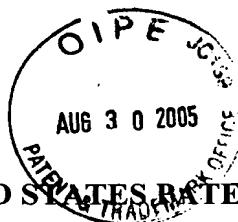
Registration No. 28,870

Customer Number

22850

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(OSMMN 05/03)

Docket No.: 217804US2



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Sanae ITO, et al.

SERIAL NO.: 10/028,319

GAU: 2128

FILED: December 28, 2001

EXAMINER: K. K. Gebresilassie

FOR: SIMULATION METHOD, SIMULATION PROGRAM, AND SEMICONDUCTOR DEVICE
MANUFACTURING METHOD EACH EMPLOYING BOUNDARY CONDITIONS

LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

COMMISSIONER FOR PATENTS
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

1 Replacement Drawing Sheets _____ New Drawing Sheets

Official Action dated May 31, 2005

Notice of Allowance/Issue Fee dated _____

Other dated _____

The changes and/or modifications made include the following:

Replacement Figure 1 includes corrections required by PTO Communication.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.


Eckhard H. Kuesters

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